

# **Product Specification For LCD Module**

# Model NO. : CNKD0502-13002A

### **REVISION** : A

□ APPROVAL FOR SPECIFICATIONS ONLY

APPROVAL FOR SPECIFICATIONS AND SAMPLE

CUSTOMER :

APPROVED BY :

CNK LCM R&D CENTER						
APPROVED BY CHECKED BY PREPARED BY						
DIRECTOR	MANAGER	Engineer				

深圳市希恩凯电子有限公司 SHEN ZHEN CNK ELECTRONICS CO.,LTD 深圳市宝安区沙井南环路鸿桥工业园 2 期 B 栋 4 楼 TEL:0755-28024001,29761676 FAX:0755-28021718

http://www.szcnk.com



MODEL NO.

PAGE

2. TABLE OF CONTENTS								
NO	CONTENTS	PAGE						
1	COVER	1						
2	TABLE OF CONTENTS	2						
3	RECORD OF REVISION	3						
4	GENERAL SPECIFICATION	4						
5	LCD ELECTRO-OPTICAL CHARACTERISTICS	5						
6	LCD OPTICAL CHARACTERISTICS	5						
7	OPTICAL CHARACTERISTICS DEFINITION	6						
8	INTERFACE PIN ASSIGNMENT	7						
9	BACKLIGHT	8						
10	BLOCK DIAGRAM	9						
11	AC Characteristics	10						
12	RELIABILITY	11						
13	INSPECTION CRITERIA	12-13						
14	PRECAUTION FOR USE OF LCD MODULE	14-15						
15	LCM DRAWING	16						



MODEL NO.

REV	COMMENT	PAGE	DATE
A	Initial Release	1-16	2013/12/25



4.	GENERAL SPECIFICA	TION
	ITEM	CONTENTS
	Module Size	193(W)×61 (H)×7.4(T) mm
	Display View Area	149(W) × 48(H) mm
	LCD Type	VA/NEGATITVE/TRANSMISSIVE
	View Angle	6 O'clock
	Driver IC	HT1621B
	Backlight Driver type	Power/WHITE
	DC to DC circuit	Build-In
	Weight	TBD



CNKD0502-13002A

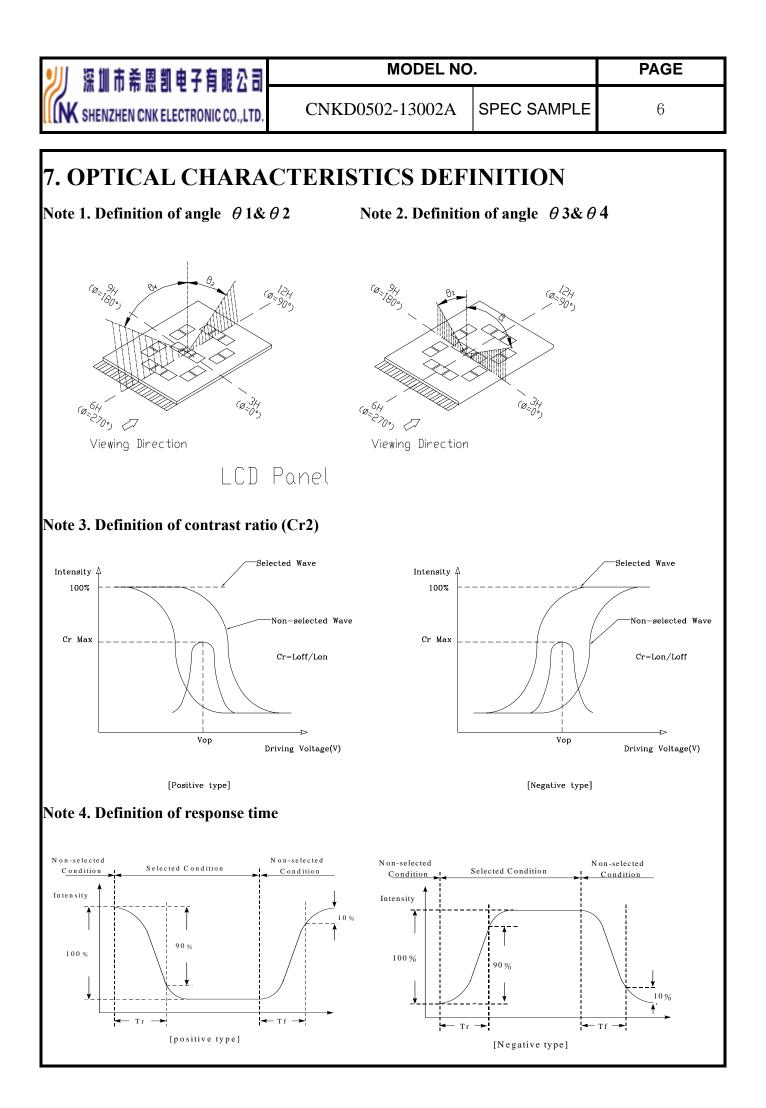
### 5. LCD ELECTRO-OPTICAL CHARACTERISTICS (Ta=25°C)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
LCD Module Driving Voltage	VDD	<b>Ta=25℃</b>	4.8	5.0	5.2	Volt
Operating Temperature	Тор		<b>-10</b> ℃	-	<b>+55</b> ℃	°C
Storage Temperature	Tst		<b>-20</b> ℃	-	<b>+65</b> ℃	°C
Humidity	%			90%		

Note: See section 12 for backlight uniformity measurement

### **6. LCD OPTICAL CHARACTERISTICS**

Item		Symbol Tomp(°C)	Rating			Unit	D . f	
		Symbol	Temp(℃)	Min	Тур	Max	– Unit	Reference
-			50					
Recomm Driving		Vop	25	4.8	5.0	5.2	v	
Driving Voltage			0					
Response	Rise Time	Tr	25		180	230	- ms	Nut
Time	Fall Time	Tf	25		180	230		Note4
Frame Fr	equency	FR	25	70	75	80	Hz	
	Ø=0°	$\theta_1$		30			– Deg	
Viewing	Ø=180°	$\theta_2$	25	30				
angle Cr≧2	Ø =90°	θ3	25		30			Note1 Note2
	Ø=270°	θ4			15			10002
Viewing Direction		tion		6 O'clock				
Contras	t Ratio	Cr	25	6	8			Note3





CNKD0502-13002A

### 8. INTERFACE PIN ASSIGNMENT

PIN	SYMBOL	FUNCTIONS
1	VDD	Supply voltage for logic circuit +5v
2	VSS	Power Ground.
3	DATA	Data bus line
4	WR	Read/write selection(H:Read,L:Write)
5	CSB	Chip selection signal B
6	Α	BACKLIGHT +
7	к	BACKLIGHT -



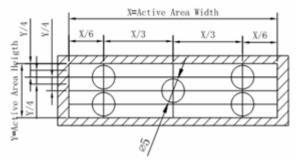
# 9. BACKLIGHT

# BACKLIGHT ELECTRICAL-OPTICAL CHARACTERISTICS (Unless specified, Ambient temperature Ta=25°C)

PARAMETER	Sym.	Min.	Тур.	Max.	Unit	Test Condition	Reference
Supply Current	I		120		mA	3.0V	
WHITE LED	V		3.0		v	120mA	
Backlight Luminous Intensity	Lv				Cd/m <sup>2</sup>	120mA	Note1
Uniformity		50			%	120mA	Note1 Note2

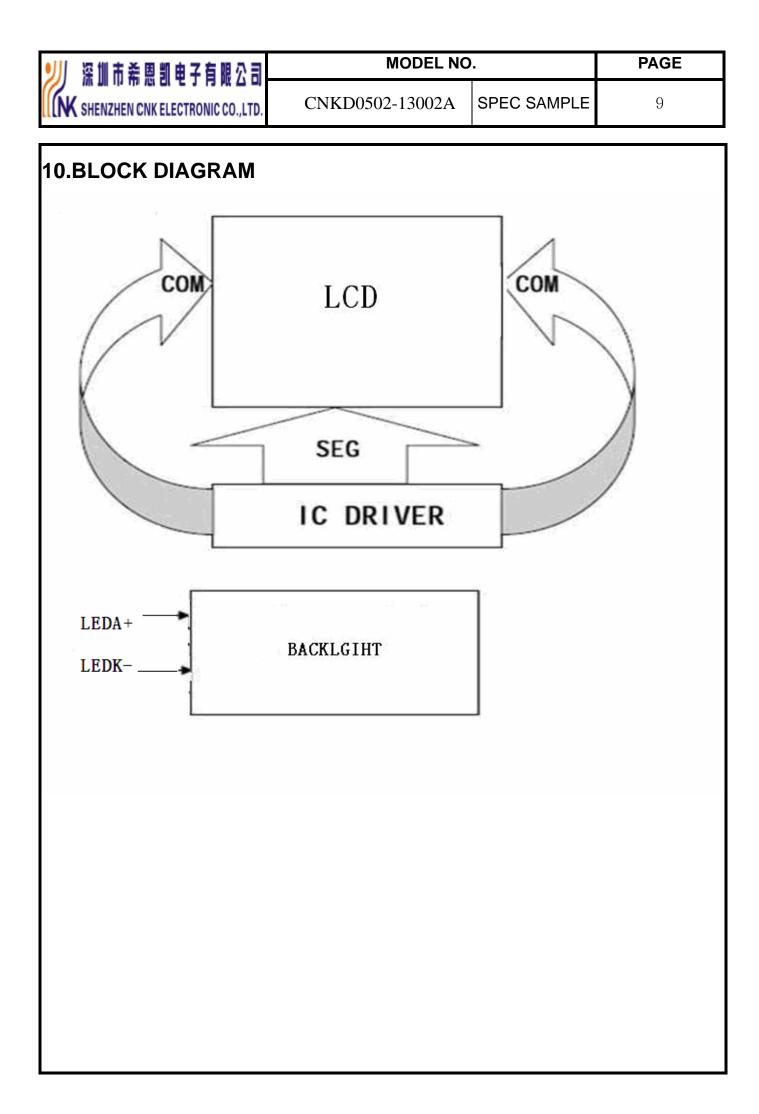
#### NOTE:

1. Backlight luminance: The measurement instrument is BM-7 luminance colorimeter. The aperture of colorimeter is ø5mm and the distance between lens and backlight is 50cm. 5 points will be measured and the luminance of backlight is the average value of 5 points.



measure point on backlight

2. Backlight Uniformity = (The Luminance min / The Luminance max ) x 100%





#### MODEL NO.

PAGE

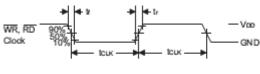
CNKD0502-13002A

SPEC SAMPLE

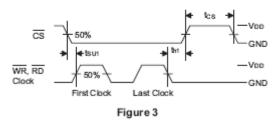
10

11.AC Characteristic	S
----------------------	---

			Test Conditions		-		
Symbol	Parameter	$V_{\text{DD}}$	Conditions	Min.	Тур.	Max.	Unit
fsys1	System Clock	_	On-chip RC oscillator	_	256	_	kHz
f <sub>SYS2</sub>	System Clock	_	Crystal oscillator	_	32.768	_	kHz
fsys3	System Clock	_	External clock source	_	256	_	kHz
		_	On-chip RC oscillator	—	f <sub>SYS1</sub> /1024	_	Hz
f <sub>LCD</sub>	LCD Clock	_	Crystal oscillator	_	f <sub>SYS2</sub> /128	_	Hz
		_	External clock source	_	f <sub>SYS3</sub> /1024	_	Hz
t <sub>COM</sub>	LCD Common Period	_	n: Number of COM	_	n/f <sub>LCD</sub>	_	s
6	Sastal Data Clack (WP ala)	3V		4	_	150	kHz
f <sub>OLK1</sub>	Serial Data Clock (WR pin)	5V	Duty cycle 50%	4	_	300	kHz
fa_K2	Octobel Data Olaski (DD alla)	ЗV	Dutumete FOW	_	_	75	kHz
ICLK2	Serial Data Clock (RD pin)	5V	Duty cycle 50%	_	_	150	kHz
f <sub>TONE</sub>	Tone Frequency	_	On-chip RC oscillator	—	2.0 or 4.0	_	kHz
tcs	Serial Interface Reset Pulse Width (Figure 3)	_	cs	_	250	_	ns
			Write mode	3.34	_	125	
	WR, RD Input Pulse Width	3V	Read mode	6.67	_	_	μs
tαLK	(Figure 1)	5V	Write mode	1.67	_	125	
		57	Read mode	3.34	_	_	μs
tr, tr	Rise/Fall Time Serial Data Clock Width (Figure 1)	_	_	_	120	_	ns
t <sub>su</sub>	Setup Time for DATA to WR, RD Clock Width (Figure 2)	_	_	_	120	_	ns
th	Hold Time for DATA to WR, RD Clock Width (Figure 2)	_	_	_	120	—	ns
t <sub>su1</sub>	Setup Time for $\overline{CS}$ to $\overline{WR}$ , $\overline{RD}$ Clock Width (Figure 3)	_	_	_	100	_	ns
t <sub>h1</sub>	Hold Time for CS to WR, RD Clock Width (Figure 3)	_	_	_	100	_	ns







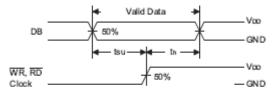


Figure 2



**12. RELIABILITY** 

CNKD0502-13002A

		<b>T</b> . <b>T</b>		The second se
	No	Test Item	Content of Test	Test
				Condition
	1	High Temperature	Endurance test of high temperature for a long time.	80°C
		Storage		96H
	2	Low Temperature	Endurance test of low temperature for a long time.	-20±2℃
		Storage		96H
	3	High Temperature	Endurance test of electrical stress (Voltage & Current)	70℃
		Operation	and the thermal stress to the element.	96H
Test	4	High Temperature	Endurance Test of high temperature and high	45±2°C
ent '		/Humidity Storage	humidity for a long time.	90±2%RH
muc				96H
Environment Test	5	Thermal shock	Endurance test of low and high temperature	-10±2°C/70±2
Ē			cycles.(air to air)	C
			$-20\pm2^{\circ}C \iff 70\pm2^{\circ}C$	10 cycle
			(60min) <i>(</i> 60min)	
			1 cycle	
	6	vibration	Maximum vibration is 2.45m/s2 (0.25 G) during	Ambient
			operation and 11.75 m/s2 (1.2 G) during storage.	temperature
			Tested 10-100KHz XYZ directions 1 hour each.	Ta=25°C
	7	shock	Maximum shock is 29.4 m/s2 (3 G) during operation	Ambient
	,	~	and 490.0 m/s2 (50 G) during storage. Tested 10	temperature
			milliseconds in XYZ directions 1 time each.	Ta=25°C

Note:

1) Condensation is not allowed during low temperature testing.

2) Driving condition for operation test:

Power Supply Current for BackLight(ImA)=15mA

#### **Failure Judgment Criterion**

After the above mentioned test (For Environmental Test, after 2 hours in room temperature):

1) There should not be conspicuous failure of display quality and appearance.

2) Contrast ratio should be greater than or equal to 50% of the initial contrast ratio.

3) Abnormal function is a failure.



CNKD0502-13002A

SPEC SAMPLE

PAGE

10 I	tem	Criteria				AQ
Elect Testi		<ul> <li>(1) non-display</li> <li>(2) segment missing</li> </ul>			0.65	
2 Dime state	ension	(3) segment short       Dimension out of the specification				1.00
3	ss crack	Substrate check symbol De X: Length direction Y: Short side direction Z: Thickness direction T: Glass thickness K:LCD length L: Single connector width (1) General crack (2) Corner (2) Corner (3) Contact pad crack (4) Substrate protuberance	X $1/8K \ge$ X $1/8K \ge$ I. Cracks on exceed $1/2$ of2. Y not to explanation of the second sec	$K \ge 1/3$ the contact an of the glass that acceed 1/3 seal	rea $Z$ r No rea check Y $ZL \ge Nocheckrea cannottickness.$	2.50

刈 深圳市希恩凯电子有限公司	MODEL NO	PAGE		
SHENZHEN CNK ELECTRONIC CO.,LTD.		SPEC SAMPLE	13	

NO	Item	Criterion				AQL	
4.	Black spot , white spot (including polarizer) Ø=(X+Y)/2	(1) Round type $\begin{array}{c} & & \\ & & \\ & & \\ & & \\ & & \\ \end{array}$ (2) Line type	accept $0.0$ $3.0 \ge L$ $0.0$	A $A$ $A$ $A$ $A$ $A$ $A$ $A$ $A$ $A$	cceptable QTY Accept 2 1 0 Acceptable QTY No check 2		1.50
u	unit:mm	2.3>1       0.000>10         0.100 <w< td="">       As round type         (3) No more than 2 spots and lines within 3 mm. Maximum combined total of round and line defects is 4.         (4) Scratches criterion is same as that of Round type.</w<>					
5.	Pixel deformation	Symbols: W: segment width Ø: average of diameter $=(A+B)/2$ (1)Pin hole and deformation $\overrightarrow{Wdth}$ Acceptable Defect $W < 0.4$ $\emptyset \le 0.20$ and $\emptyset \le 1/2W$ $W \ge 0.4$ $\emptyset \le 0.25$ and $\emptyset \le 1/3W$ Ø under 0.10mm ,acceptable (2) Pixel size should be in the range of 95% to 100% of the normal dimension and the gap between pixels should be less than 150% of normal dimension.			2.5		
6.	Polarizer bubble Ø=(X+Y)/2		size $\emptyset$ $\emptyset \leqslant 0.20$ $0.20 < \emptyset \leqslant 0.4$ $0.50 < \emptyset \leqslant 1.0$ $1.00 < \emptyset$ Total QTY $0.20 < \emptyset < 1.0$	50	cceptable QTY No check 3 2 0 3		1.5
7.	Contrast	Under normal pov	ver supply, uneven c		is unacceptable.		2.5
8.	Rainbow	Obvious uneven c	olor in LCD viewin	g area is	s not allowed.		2.5

## 14. PRECAUTION FOR USE OF LCD MODULE

#### 1. Handling Precautions

- 1) The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 2) If the display panel is damaged, the liquid crystal substance leaks out ,do not ingest. If the substance contacts skin or clothes, promptly wash off using soap and water.
- 3) Do not apply excessive force to the display surface or adjoining areas since this may affect the LCD color
- 4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 5) If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - --Isopropyl alcohol
  - --Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

--Water

--Ketone

--Aromatic solvents

6) Do not attempt to disassemble or process the LCD module.

### 2. Assembling Precautions

- When mounting the LCD module make sure that it is free of twisting, warping, and distortion. Distortion has great influence upon display quality. Also, use an adequately stiff outer case.
- 2) Please handle the LCD module by its side.
- 3) NC terminal should be open. Do not connect anything.
- 4) If the logic circuit power is OFF, do not apply the input signals.
- 5) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

•Be sure to ground the body when handling the LCD module.

·Tools required for assembly, such as soldering irons, must be properly grounded.

•To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

•The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

6) Be careful handling the glass panel because it has a very sharp edge.

### 3. Storage Precautions

- When storing the LCD module, avoid exposure to direct sunlight, to the light of fluorescent lamps, to high temperature or to high humidity. Whenever possible, LCD modules should be stored in the same packaging they were shipped in.
- 2) Exercise care to minimize corrosion of the electrodes. Corrosion of the electrodes is accelerated by water droplets or by current flow in a high-humidity environment.

### 4. Design Precautions

- The absolute maximum ratings represent the rated value beyond which LCD module can not exceed. When the LCD modules are used in excess of this rated value, their operation characteristics may be adversely affected.
- To prevent the occurrence of erroneous operation caused by noise, attention must be paid to satisfy V<sub>IL</sub>, V<sub>IH</sub> specification values including taking the precaution of using signal cables that are short.
- 3) The LCD exhibits temperature dependency characteristics. Since recognition of the display becomes difficult when the LCD is used outside its designated operating temperature range, be sure to use the LCD within this range. Also keep in mind that the LCD driving voltage levels necessary for clear displays will vary according to temperature.
- 4) We recommended that power supply lines (VDD) have over-current protection line. (Fuse etc. Recommend Value:0.5A)
- 5) Sufficiently reduce electrical noise from peripheral devices.
- 6) To cope with EMI, take measures basically on outputting side.
- 7) Assemble LCD module tightly with the application case or PCB.

### 5. Other considerations

- Liquid crystal solidifies under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the LCD module is subjected to a strong shock at a low temperature.
- 2) If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- 3) To minimize the performance degradation of the LCD module's resulting from destruction caused by static electricity, etc., exercise care to avoid touching the LCD's electrical connections.
- 4) LCD voltage adjustment may be necessary to obtain the best contrast on each LCD.
- 5) Precaution for disposal of LCD module. When disposal of LCD module, ask specialization company of industrial waste which is permitted by the government. When burn up LCD module, obey the law of environmental hygienic.

>	由 Autodesk 教育版产品制作 □□	
15.LCM DRAWING	v ,	
DISPLAY MODE : VIEWING DIRECTION : 0 OPERATING TEMP : - STORAGE TEMP : - FRONT POLARIZER : T BACK POLARIZER	) ] ] ] ] ] ]	ω
VA, NEGATIVE 1/4 DUTY, 1 6 O'CLOCK -20°C TO +5 -20°C TO +6 TRANSMISSIVE PIN PIN PIN PIN Naterials of the Ne RoHS instri-	5 5 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	7
MODE /3 BIAS,5.0 V, 64Hz 55°C S5°C ADHESIVE , ADHESIVE , ADHESIVE uctions of EU. 6		თ
教育版产品制作		A vcanônu ⊞
日本 日本 日本 日本 日本 日本 日本 日本 日本 日本		REV:
ч Модо Полоничини и на	8.00 MIN V.A 48.00 51.00BL 52.00±0.2 57.00±0.2 51.00	۵ ۲. ۵
※別市希恩凯电子有限公司 SHENZHEN CMK ELECTRONIC 00., LTD. MODEL NUMBER: SCALE: UNIT GENERAL TOL:± 0.2 UNIT DO NOT SCALE THIS DRAWING. 3 2 1 1		2 DESCRIPTION
思想  思想  思いて、   RK ELECTRONIC   SCALE:   GENERAL TOL:±   OF NOT SCALE THIN   2   2	•	
恩凯电子有限公司 5 CSB 6 A 5 CSB 6 A 5 CSB 6 A 5 CSB 6 A 7 K 7 K 7 K 7 K 7 K 7 K 7 K 7 K 7 K 7 K	PIN NAME	P
>	→怫品 <del>゛</del> 狱育殘 <b>≯ɛэboîuA</b> 由	